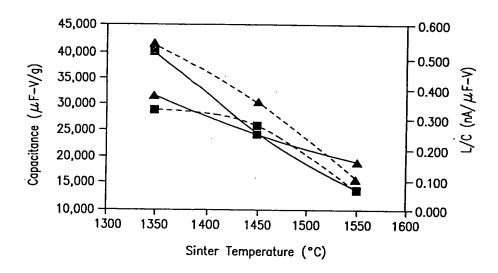


Tantalum Silicon and 50K vs Sinter Temperature (50 $V_{\mathrm{f}}$ ) Chart 1



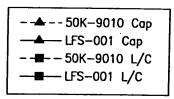


FIG. I



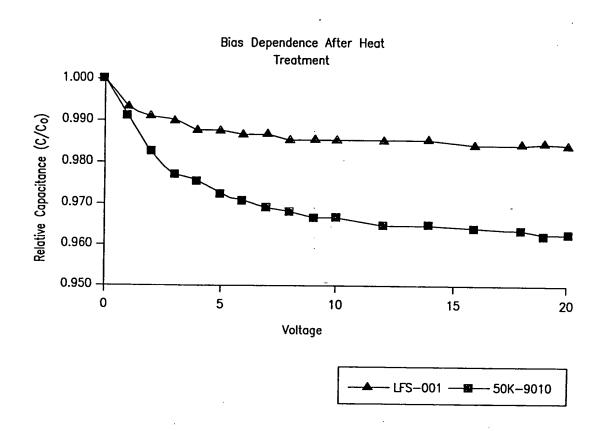


FIG. 2



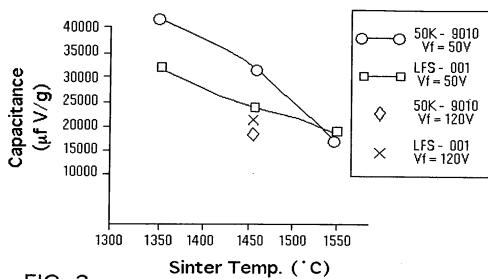


FIG. 3

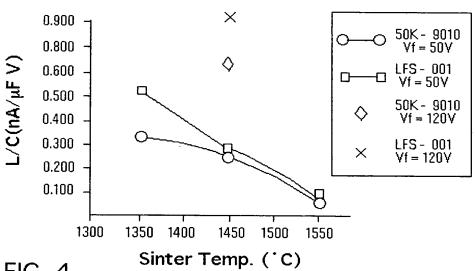
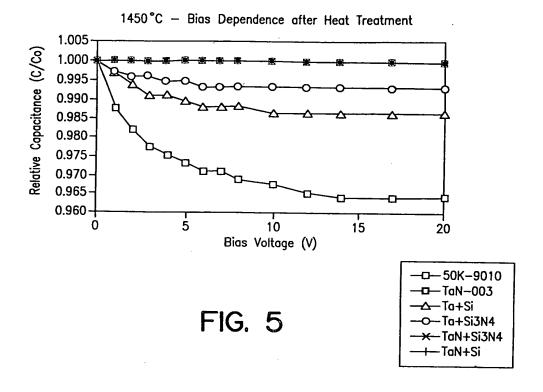


FIG. 4





1.005 Relative Capacitance 0.995 0.99 0.985 0.98 0.975 0.97 0.965 5 15 Ò 10 20 Bias Voltage (V) ■—TaN+Si - TaN+Si3N4 FIG. 6 <del>×−</del> Ta+Si3N4 -O-- Ta+Si

→ 50K9010

1350°C — Bias Dependence after Heat Treatment



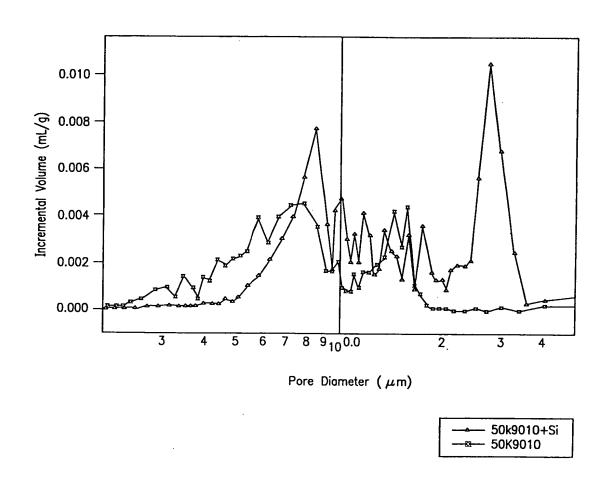


FIG. 7



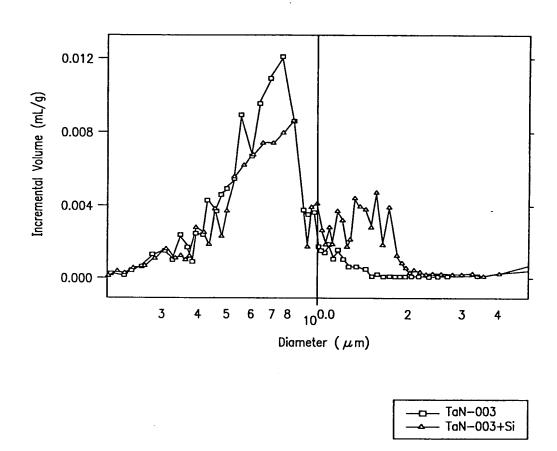


FIG. 8